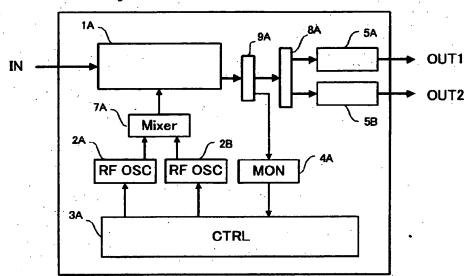
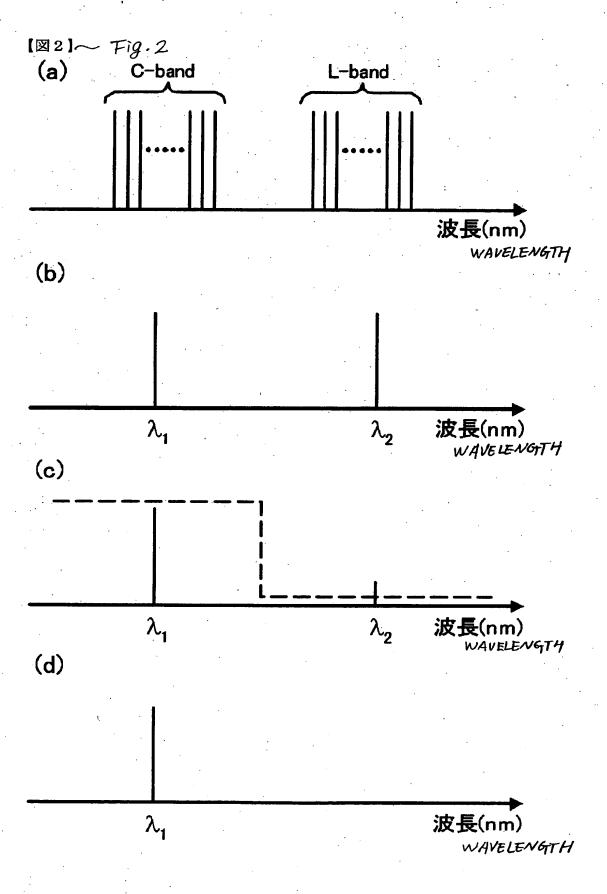
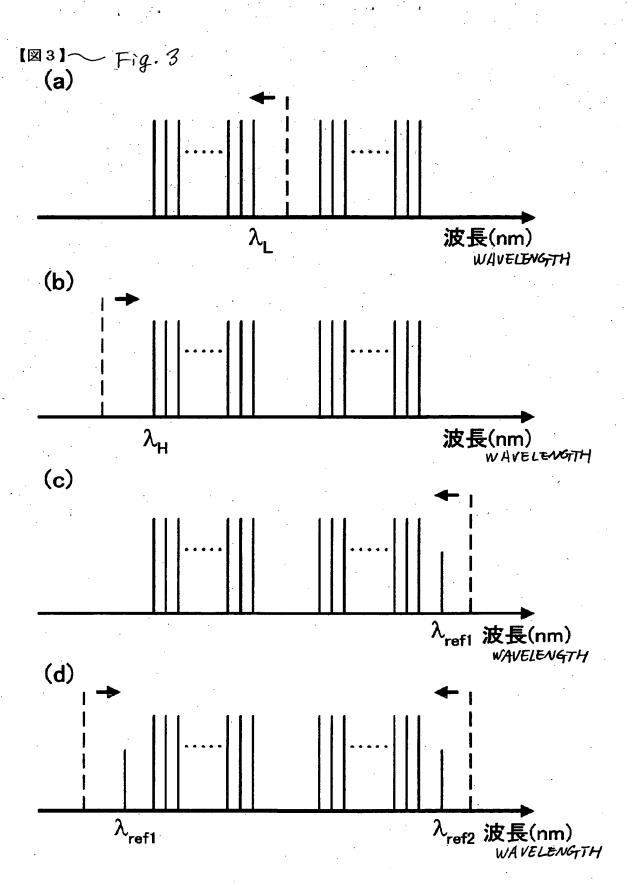
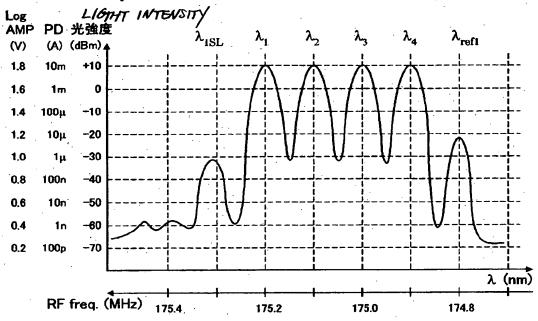
[图1] Fig. 1







[図4] Fig. 4



[図5]~ Fig. 5

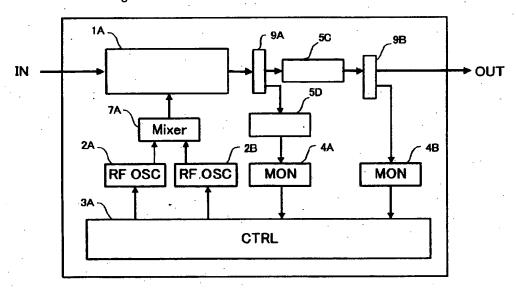
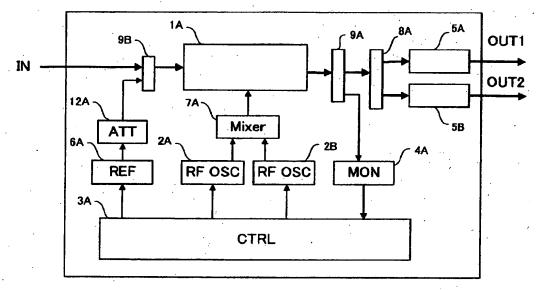
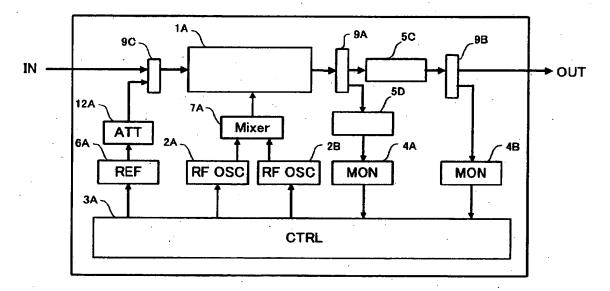


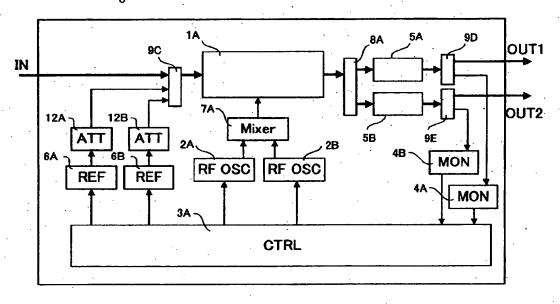
图6~Fig.6



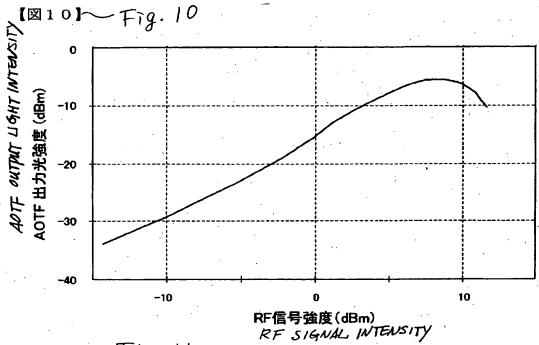
[图7]~Fig.7

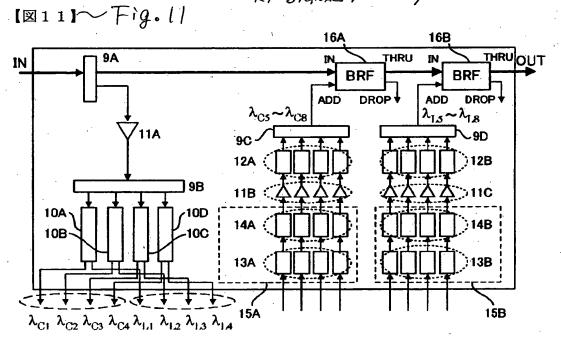


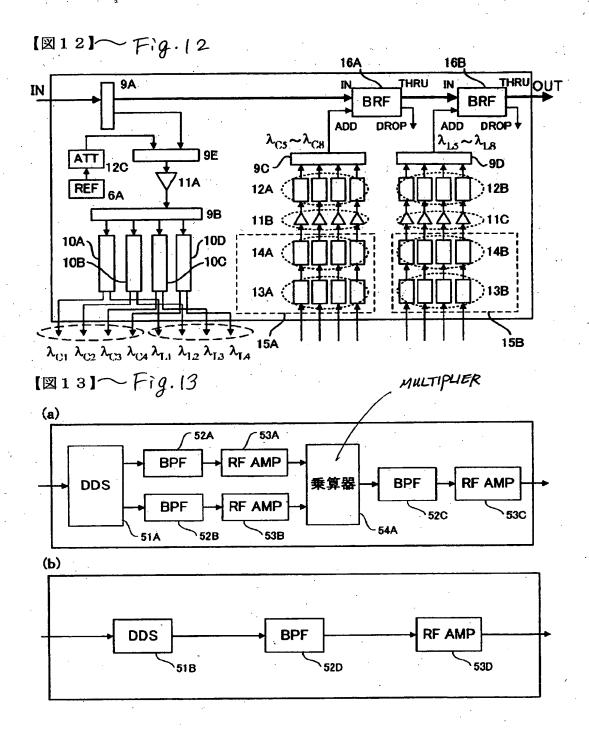
[18] Fig. 8

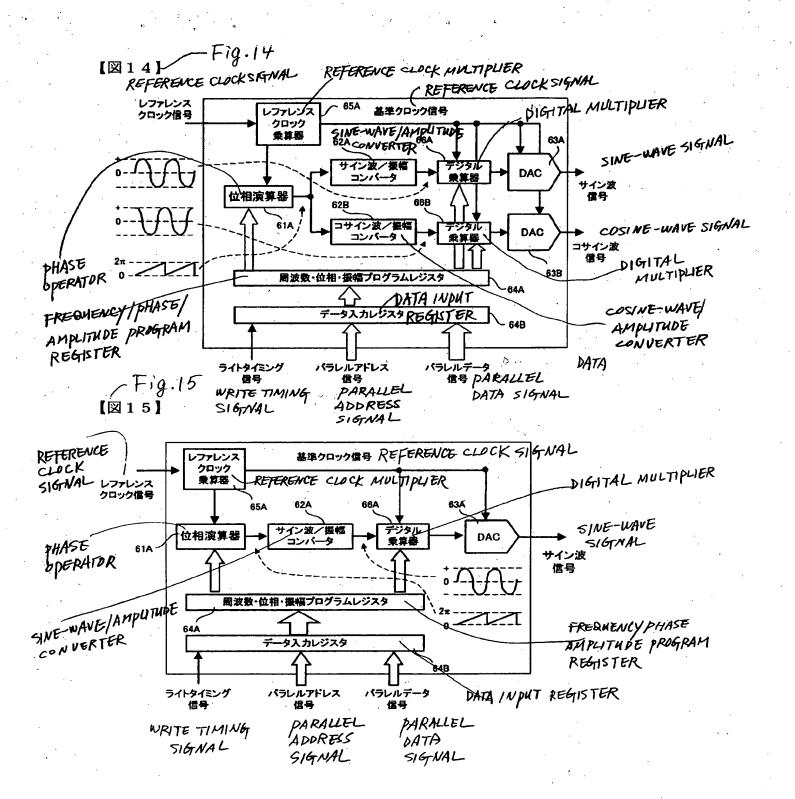


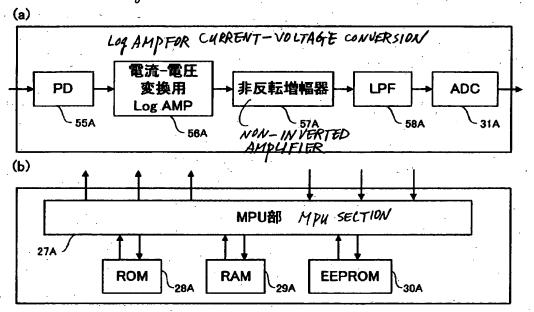
- Fig. 9 [図9] OUTI 9B 1A OUT2 9C [N 1B OUT3 9D → OUT4 1C **A**II **~5E** ATT 1D ATT ~5G 12B 9E REF REF 6B RF OSC 5H RF OSC RF OSC 3A-2D RF OSC 2E-RF OSC **RF OSC** CTRL ~4A MON `4B MON ^4C MON 4D MON `4E MON 4F MON



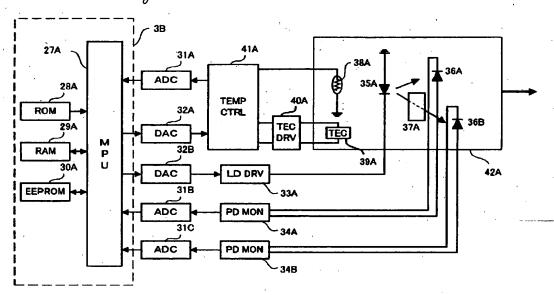


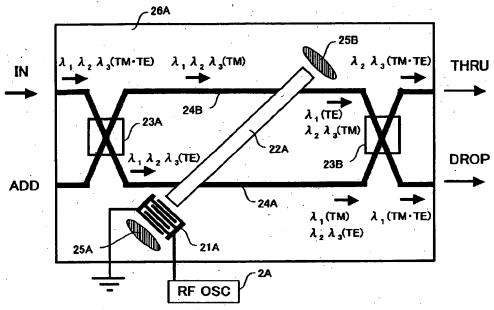




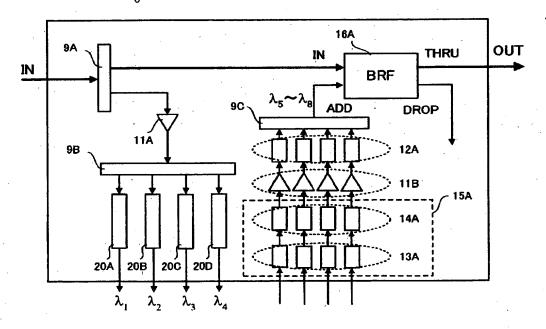


[図17]~Fig.17





[図19]~ Fig.19



[图20] Fig. 20

